

INTERFACE CIRCUIT - RELAY AND LAMP-DRIVER

- HIGH OUTPUT CURRENT
- ADJUSTABLE SHORT-CIRCUIT PROTEC-TION
- THERMAL PROTECTION WITH HYSTERESIS TO AVOID THE INTERMEDIATE OUTPUT LEV-FLS
- LARGE SUPPLY VOLTAGE RANGE: +8 V to +45 V

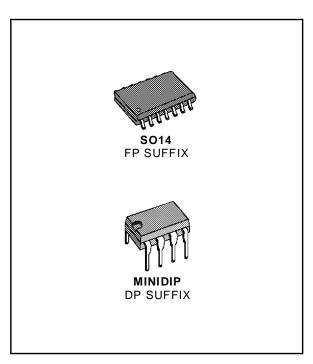
DESCRIPTION

The TDE1737-TDF1737 is a monolithic amplifier designed for high current and high voltage applications, specifically to drive lamps, relays and control of stepper motors.

This device is essentially blow-out proof. Current limiting is available to limit the peak output current to a safe value, the adjustment only requires one external resistor. In addition, thermal shut down is provided to keep the I.C. from overheating. If internal dissipation becomes too great, the driver will shut down to prevent excessive heating.

The output is also protected against short-circuits with the positive power supply.

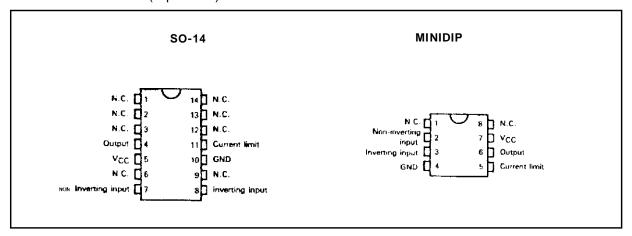
The device operates over a wide range of supply voltages from standard \pm 15 V operational amplifier supplies down to the single + 12 V or + 24 used for industrial electronic systems.



ORDER CODES

Part	Temperature	Package			
Number	Range	DP	FP		
TDE1737	− 25 °C to + 85 °C	•	•		
TDF1737	– 40 °C to + 85 °C	•	•		
Example: TDE1737DP					

PIN CONNECTIONS (top views)



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ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Value	Unit
Vs	Supply Voltage	50	V
V_{I}	Input Voltage	50	V
V_{ID}	Differential Input Voltage	50	V
lo	Output Current	1000	mA
P _{tot}	Power Dissipation	Internally Limited	W
T _{oper}	Operating Free-air Temperature Range for TDE1737	- 25 to + 85	°C
T _{oper}	Operating Free-air Temperature Range for TDF1737	- 40 to + 85	°C
T _{stg}	Storage Temperature Range	- 65 to + 150	°C

THERMAL CHARACTERISTICS

Symbol	Parameter			Value	Unit	
R _{th(j-c)}	Maximum Junction-case Thermal Resistance MINIDIF			MINIDIP	50	°C/W
R _{th(j-a)}	Maximum Junct MINIDIP	ion-ambient	Thermal	Resistance	120	°C/W
	Junction-ceramic Substrate (case glued to substrate)			SO14	90	°C/W

ELECTRICAL CHARACTERISTICS

 $\textbf{TDE1737} - 25 \text{ °C} \leq T_{amb} \leq + 85 \text{ °C}, \ \, + 8 \text{ V} \leq V_{CC} \leq + 45 \text{ V}, \ \, I_O \leq 300 \text{ mA}, \ \, T_j \leq + 150 \text{ °C}$ (unless otherwise specified)

TDF1737 $-40~^{\circ}\text{C} \le T_{amb} \le +85~^{\circ}\text{C}, +8~\text{V} \le \text{V}_{CC} \le +45~\text{V}, lo \le 300~\text{mA}, T_j \le 150~^{\circ}\text{C}$

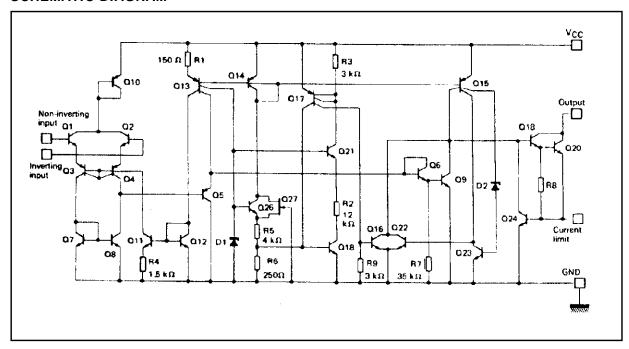
Symbol	Parameter	Min.	Тур.	Max.	Unit
V _{IO}	Input Offset Voltage – (note 1)	ı	2	50	mV
I _{IB}	Input Bias Current	ı	0.1	1.5	μΑ
Icc	Supply Current ($V_{CC} = +24 \text{ V}, I_{O} = 0$)	_	3	5	mA
V_{CM}	Common-mode Input Voltage Range	2	-	V _{CC} -2	V
I _{SC}	Short-circuit Current Limit ($R_{SC} = 1.5 \Omega$, $T_{case} = +25 ^{\circ}C$)	_	500	_	mA
V _{CC} -V _O	Output Saturation Voltage (output low) $(V_1^+ - V_1^- \ge 50 \text{ mV } I_0 = 300 \text{ mA}, R_{SC} = 0)$	-	1	1.5	V
I _{OL}	Output Leakage Current (output high) ($V_O = V_{CC} = + 24 \text{ V}$, $T_{amb} = + 25 \text{ °C}$)	_	_	10	μΑ

Notes: 1. The offset voltage given is the maximum value of input voltage required to drive the output voltage within 2 V of the ground or the supply voltage.

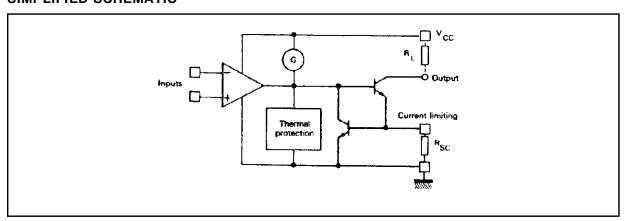
2. Devices bonded on a 40 cm² glass-epoxy printed circuit 0.15 cm thick with 4 cm² of cooper.



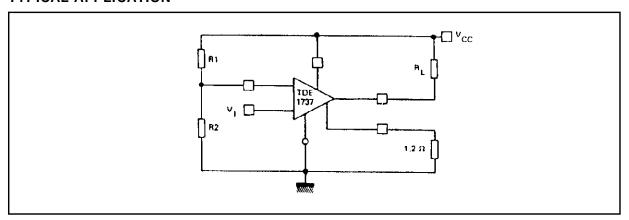
SCHEMATIC DIAGRAM



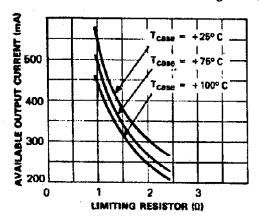
SIMPLIFIED SCHEMATIC



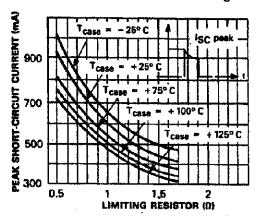
TYPICAL APPLICATION -



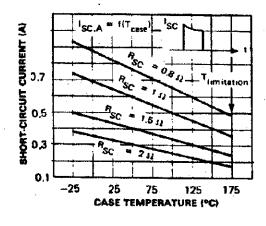
Available output current versus limiting resistors



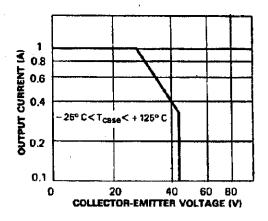
Peak short-circuit current versus limiting resistor



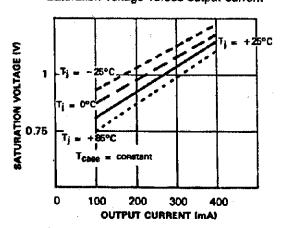
Short-circuit current versus case temperature



Safe operating area (non repetitive overload)

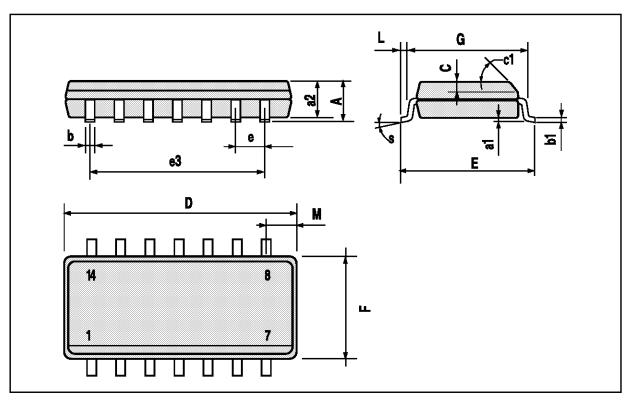


Saturation voltage versus output current



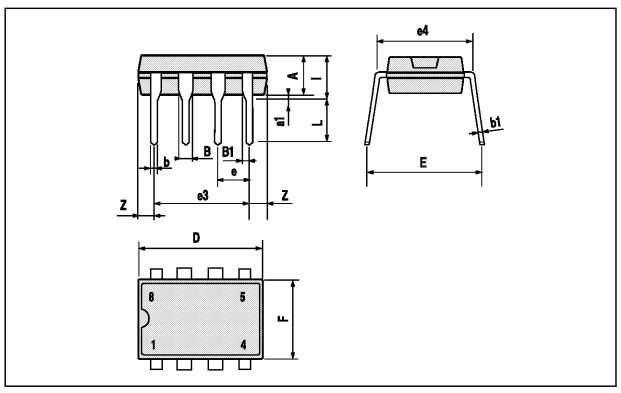
SO14 PACKAGE MECHANICAL DATA

DIM.		mm		inch		
Diffi.	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
А			1.75			0.069
a1	0.1		0.25	0.004		0.009
a2			1.6			0.063
b	0.35		0.46	0.014		0.018
b1	0.19		0.25	0.007		0.010
С		0.5			0.020	
c1			45 ((typ.)		
D	8.55		8.75	0.336		0.344
Е	5.8		6.2	0.228		0.244
е		1.27			0.050	
e3		7.62			0.300	
F	3.8		4.0	0.15		0.157
L	0.4		1.27	0.016		0.050
М			0.68			0.027
S	8 (max.)					



MINIDIP PACKAGE MECHANICAL DATA

DIM.		mm		inch		
Dim.	MIN.	TYP.	MAX.	MIN.	TYP.	MAX.
А		3.32			0.131	
a1	0.51			0.020		
В	1.15		1.65	0.045		0.065
b	0.356		0.55	0.014		0.022
b1	0.204		0.304	0.008		0.012
D			10.92			0.430
E	7.95		9.75	0.313		0.384
е		2.54			0.100	
e3		7.62			0.300	
e4		7.62			0.300	
F			6.6			0.260
ı			5.08			0.200
L	3.18		3.81	0.125		0.150
Z			1.52	_	_	0.060



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